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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10086214	FILING DATE 02/27/2002	CLASS 714	SUBCLASS 716	GAU 2184	EXAMINER Decady
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**CONTINUING DATA VERIFIED:
THIS APPLN CLAIMS BENEFIT OF 60/272,064 03/01/2001

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO 3175-Z		
TITLE : Method and apparatus for diagnosing failures in an integrated circuit using design-for-debug (DFD) techniques		
U.S.DEPT. OF COMM./PAT. & T.M.-PTO-436L (Rev 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drawg.	Fig.s Drawg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE		Application Examiner
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